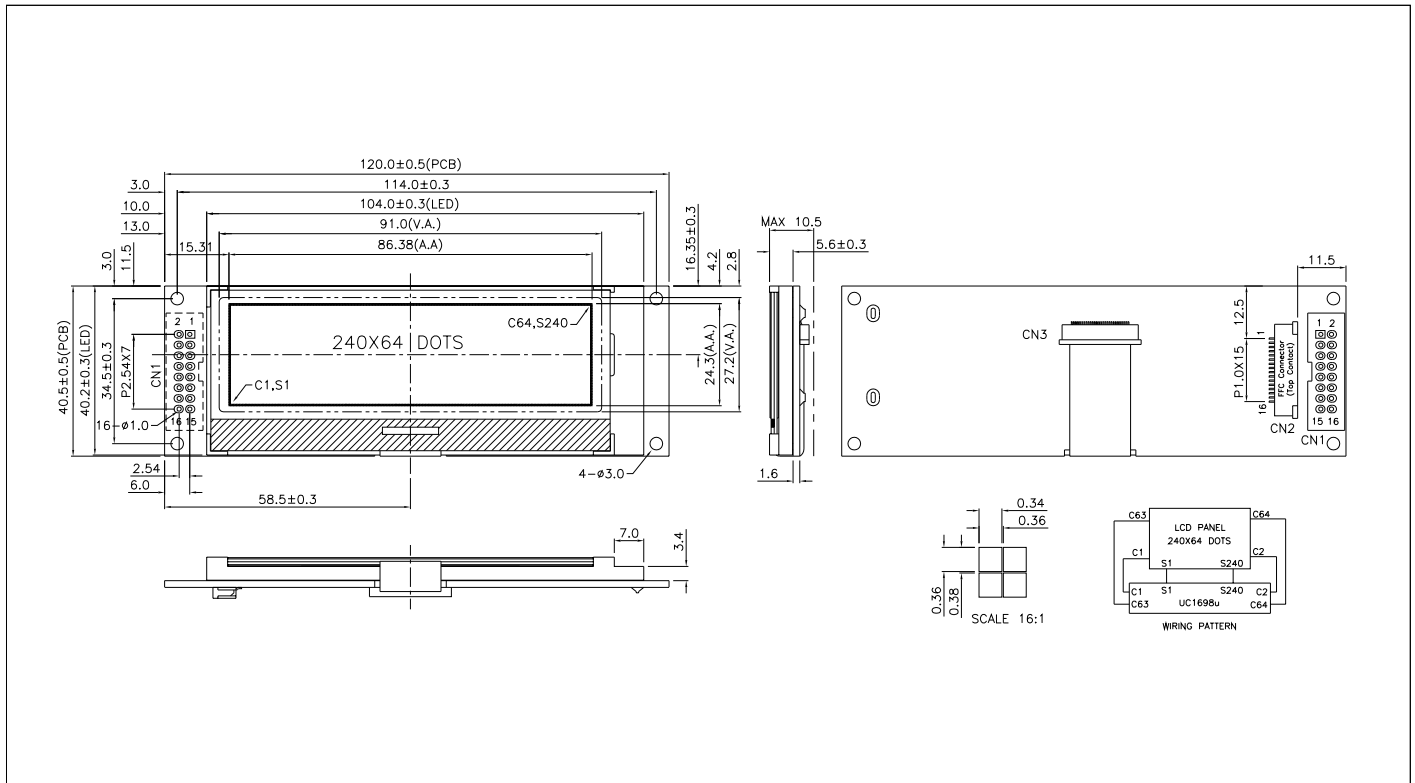


LG240645-DW

240 x 64 dots + white led backlight, 8-bit parallel or SPI, 3.3V to 5.5V



ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Min.	Max.	Unit
Supply Voltage(Logic)	$V_{DD} - V_{SS}$	-0.3	6.0	V
Supply Voltage(LCD)	$V_{LCD} - V_{SS}$	-0.3	19.8	V
Input Voltage	V_I	-0.3	$V_{DD} + 0.5$	V
Operating Temp.	T_{opr}	-20	70	°C
Storage Temp.	T_{stg}	-30	80	°C

MECHANICAL DATA

Item	Nominal Dimensions	Unit
Module Size (W x H x T)	120.0 x 40.5 x 10.5	mm
Viewing Area (W x H)	91.0 x 27.2	mm
Dot Pitch (W x H)	0.36 x 0.38	mm
Dot Size (W x H)	0.34 x 0.36	mm
Weight	Approx. 48	g

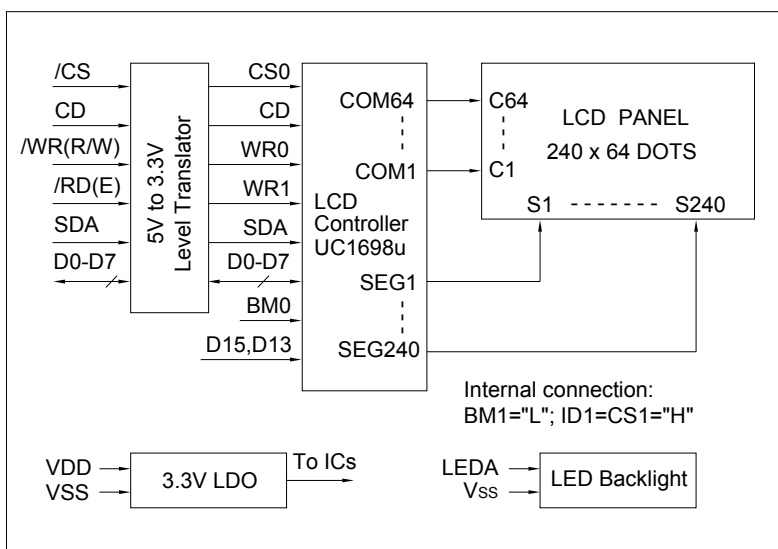
ELECTRICAL CHARACTERISTICS ($V_{DD}=3.3V$ to 5.5V)

Item	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Input High Voltage	V_{IH}	--	2.0	--	V_{DD}	V
Input Low Voltage	V_{IL}	--	0	--	0.8	V
Output High Voltage	V_{OH}	$I_{OH} = -0.1mA$	$V_{DD}-0.2$	--	V_{DD}	V
Output Low Voltage	V_{OL}	$I_{OL} = 0.1mA$	0	--	0.2	V
Supply Current	I_{DD}	$V_{DD} = 5.0V$	--	2.2	3.0	mA
LCD Driving Voltage	$V_{LCD}-V_{SS}$	$T_a=25^\circ C$	--	13.2	--	V

PIN CONNECTIONS

Pin	Symbol	Level	Function
1	V_{SS}	0V	GND
2	V_{DD}	3.3 to 5.5V	Power supply for logic
3	LEDA	5V	Power supply for LED backlight LEDK is connected to V_{SS} on PCB
4	CD	H/L	CD="H": Display data CD="L": Instruction code
5	/CS	L	Chip selection signal. Active "L".
6	SDA/D8	--	Serial data input
7	SCK/D0	H/L	In parallel mode: D0 to D7 are 8-bit bidirectional data bus. In serial mode: D0 is serial clock input (SCK). Keep D1 to D7 open in serial mode.
8	D1	H/L	
9	D2	H/L	
10	D3	H/L	
11	D4	H/L	
12	D5	H/L	
13	D6	H/L	
14	D7	H/L	
15	/WR(R/W)	H/L	/WR for 80 MPU, R/W for 68 MPU
16	/RD(E)	H/L	/RD for 80 MPU, E for 68 MPU

BLOCK DIAGRAM



LED BACKLIGHT SPECIFICATIONS ($T_a=25^\circ C$)

Item	Symbol	Typ.	Max.	Unit
Forward Voltage	V_f	3.1	3.3	V
Forward Current	I_f	45	--	mA
LED Color		White		